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resolution nanoscale technological allows precise lateral and vereither impossible of impractic computer controlled position	ogy system can remove/deposition and crossical by conventional cleaving ching and ultrafine machining deposition and substitutional cleaving ching and ultrafine machining depositions.	it material on submicro is sections of device fea or lapping techniques. eposition, the FIB syste	by has been installed. This ultrahigh in lateral and vertical scales, and tures and structural defects that are Using gallium LMI technology, are can perform multiple crossical analysis, and prepare ultra
manufacturing development.	Additional, more general rese	earch areas include ana	ted to advanced electronic device lysis of ceramic materials, titanium ten metal-ceramic interactions.
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#### STATEMENT OF PROBLEM STUDIED

The focussed ion beam workstation has been used to prepare precisely positioned cross sections of interfaces for TEM analysis. Materials and systems studied include buried defects in GaAs devices, failure analysis in active matrix electroluminescent conventional displays on glass, micro-displays directly on silicon, gate oxide interfaces in silicon based devices, and multilayered microjoining interfaces between glass-metallization-solder systems. Precise cross sectioning of these interfaces is essential for TEM analysis of the interface. The focussed ion beam has been used to develop cross sectioning techniques without mechanical sectioning, lapping, electrochemical thinning or even bulk ion beam machining. Current efforts are directed at removal of FIB produced electron transparent thin areas from the device such that they can be directly inserted in the TEM/STEM.

#### SUMMARY OF MOST IMPORTANT RESULTS

The use of the FIB workstation has allowed identification of buried micron size contaminants in shorted micro-capacitors on GaAs devices. The FIB was able to precisely remove layers and to isolate by micro machining around the contaminant for subsequent TEM analysis. Similar analysis has been carried out to identify failure mechanisms of active matrix electroluminescent micro-displays built directly on Silicon and on more conventional electroluminescent display thin film stacks on glass. The micro "rewiring" capability of the FIB has been used to rewire microelectronic devices to test redesigns of the device without the time and expense of fabrication system changes.

The FIB has been used to prepare TEM cross sections of SiGe nanostructures deposited onto Silicon by Pulsed U.V. Laser induced epitaxy and to determine the high resolution microstructures developed during metallization and heat treatment of thin gate oxide materials for use in submicron MOSFETS.

### LIST OF PUBLICATIONS AND TECHNICAL REPORTS

- ◆ Process Development for Si Based Nanostructures using Pulsed U.V. Laser Induced Epitaxy, Chaodan Deng, Ph.D. Thesis, OGI, Oct 1995.
- ◆ The Microstructural Effects of Metallization and Heat Treatment on Thin Gate Oxide for Use in Sub-micron MOSFETS, John McCarthy, Ph.D. thesis, OGI, November 1995.
- ◆ Microstructural Characterization of Al-.5Cu and Al-1Si on 0.6nm TCA SiO²/Si
  Following Heat Treatment at 400°C in N₂, Jack McCarthy, MRS Symposium Proc
  V382, Apr 95, Structure and Properties of Multilayered Thin Films.

# LIST OF PARTICIPATING SCIENTIFIC PERSONNEL

Name	Position	Degree obtained
Jack McCarthy	Professor, OGI	Ph.D. Degree
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Daya Sing	Post-Doctoral Fellow	
Lu Fang	Sr. Research Associate	
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Ajay Chaddha	Completed Studies	Ph.D. Degree
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Chaodan Deng	Studies Completed	Ph.D. Degree
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David Grainger	Professor, Colorado State	

## REPORT OF INVENTIONS

None

# **BIBLIOGRAPHY**

See List of Publications/ Presentations/ Reports

### **APPENDIXES**

None

#### TITLE

### FOCUSSED ION BEAM WORKSTATION FACILITY

FINAL PROGRESS REPORT

AUTHOR(S)

WILLIAM E. WOOD

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**FEBRUARY 1996** 

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  - A. STATEMENT OF THE PROBLEM STUDIED
  - **B. SUMMARY OF THE MOST IMPORTANT RESULTS**
  - C. LIST OF ALL PUBLICATIONS AND TECHNICAL REPORTS
  - D. LIST OF ALL PARTICIPATING SCIENTIFIC PERSONNEL SHOWING ANY ADVANCED DEGREES EARNED BY THEM WHILE EMPLOYED ON THE PROJECT\*
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